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Docket JP920010326US1

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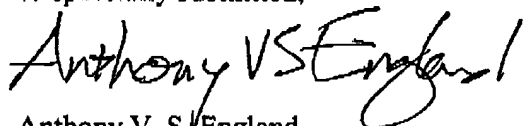
Appl. No.: 10/736,343
Filed: December 15, 2003**In the United States Patent and Trademark Office**

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|-------------------------------|---|----------------------------|
| In re the application of: |) | |
| Rajendra K. Bera |) | |
| |) | |
| Filed: 12/15/2003 |) | Group Art Unit: 2183 |
| |) | |
| For: Run-Time Parallelization |) | Examiner: Ryan Paul Fiegle |
| of Loops in Computer |) | |
| Programs |) | |
| |) | |
| Appl. No.: 10/736,343 |) | |
| |) | |
| Applicant's Docket: |) | |
| JP920010326US1 |) | |

PETITION FOR EXTENSION OF TIME

The three month statutory shortened period for filing a reply to Office action in the subject application expired on August 5, 2006. The Applicant hereby petitions the Commissioner for Patents to extend, by one month, the time for filing the reply from August 5, 2006 to September 5, 2006. This Petition accompanies a Notice of Appeal and a Fee Transmittal which authorize the Director to charge fees to cover the cost of the extension

Respectfully submitted,



Anthony V. S. England
Attorney for Applicants
Registration No. 35,129
512-477-7165
a@aengland.com

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